

*EGR424 S'08*  
*Assignment #2*

Due Date: **Tuesday, May 20, 4:00 PM**

1. Read the following sections from the ARM Architecture Reference Manual (ARM-ARM) available at:

<http://claymore.engineer.gvsu.edu/egr326/AT91SAM7S>

- Pages A5-2 to A5-33

These pages describe the important addressing modes for data processing instructions and for the most common load and store instructions that allow memory accesses.

- The following ARM instructions (starting on page A4-1):

- ADC
- B, BL
- BIC
- CLZ
- CMN
- LDR
- LDRB
- LDRH
- LDRSB
- LDRSH
- MLA
- MOV
- MRS
- MSR
- MVN
- RSB
- RSC
- SBC
- STR
- STRB
- STRH
- SUB
- SWP
- SWPB
- TEQ
- TST
- UMLAL
- UMULL

Some of the most commonly used ARM instructions are the loads (LDR, LDRB, etc.), stores (STR, STRB, etc.) and MOV instructions. Make sure you understand these instructions well along with the various addressing modes that are used with them.