

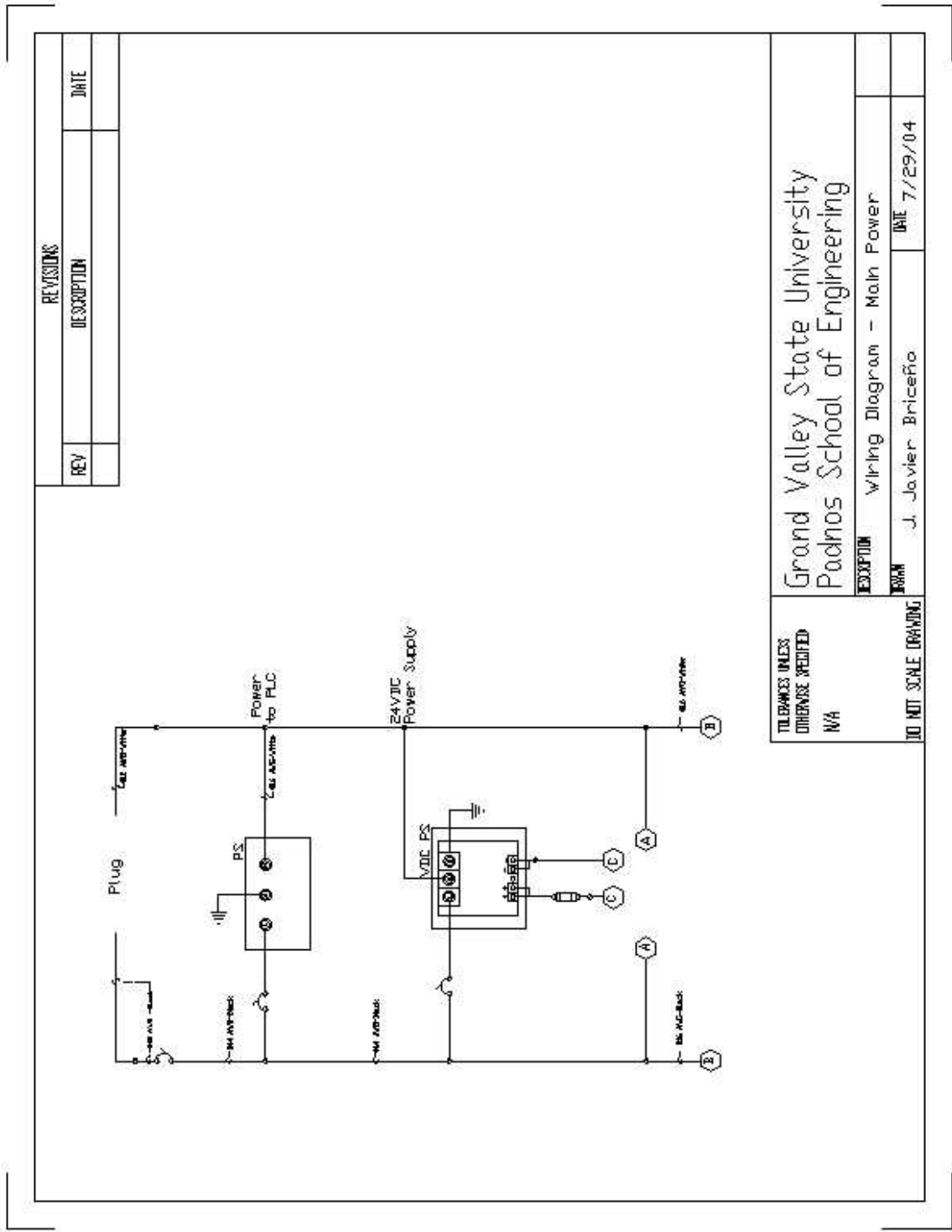
REVISIONS	
REV	DESCRIPTION

TOLERANCES UNLESS OTHERWISE SPECIFIED
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DO NOT SCALE DRAWING

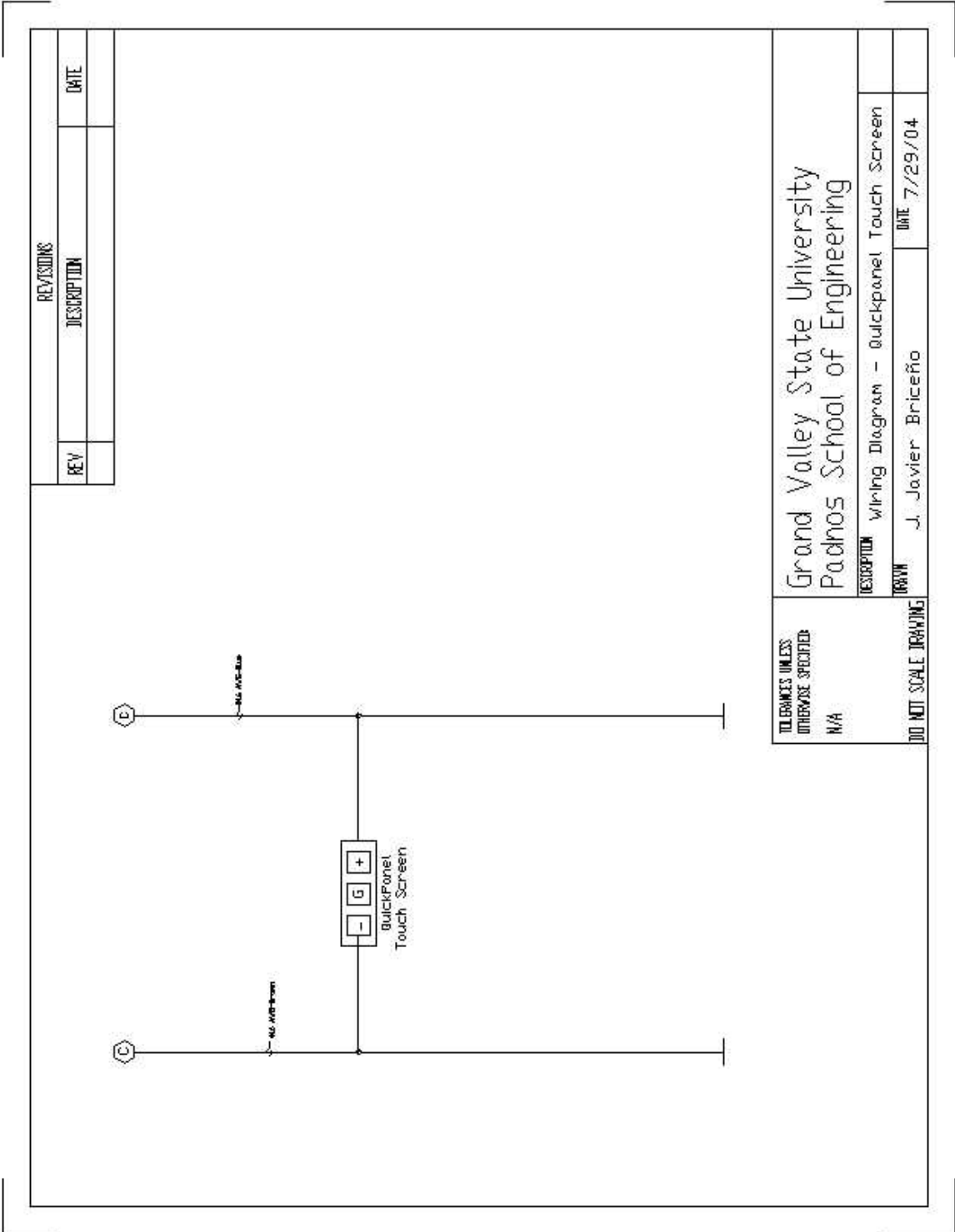
Grand Valley State University
 Padnos School of Engineering

DESCRIPTION Wiring Diagram - Input Cards
DRAWN J. Javier Briceño
DATE 7/29/04



REVISIONS		
REV	DESCRIPTION	DATE

TOLERANCES UNLESS OTHERWISE SPECIFIED	Grand Valley State University	
N/A	Padnos School of Engineering	
100 MIT SCALE DRAWING	DESCRIPTION	Wiring Diagram - Main Power
	DRAWN	J. Javier Bricefio DATE 7/29/04



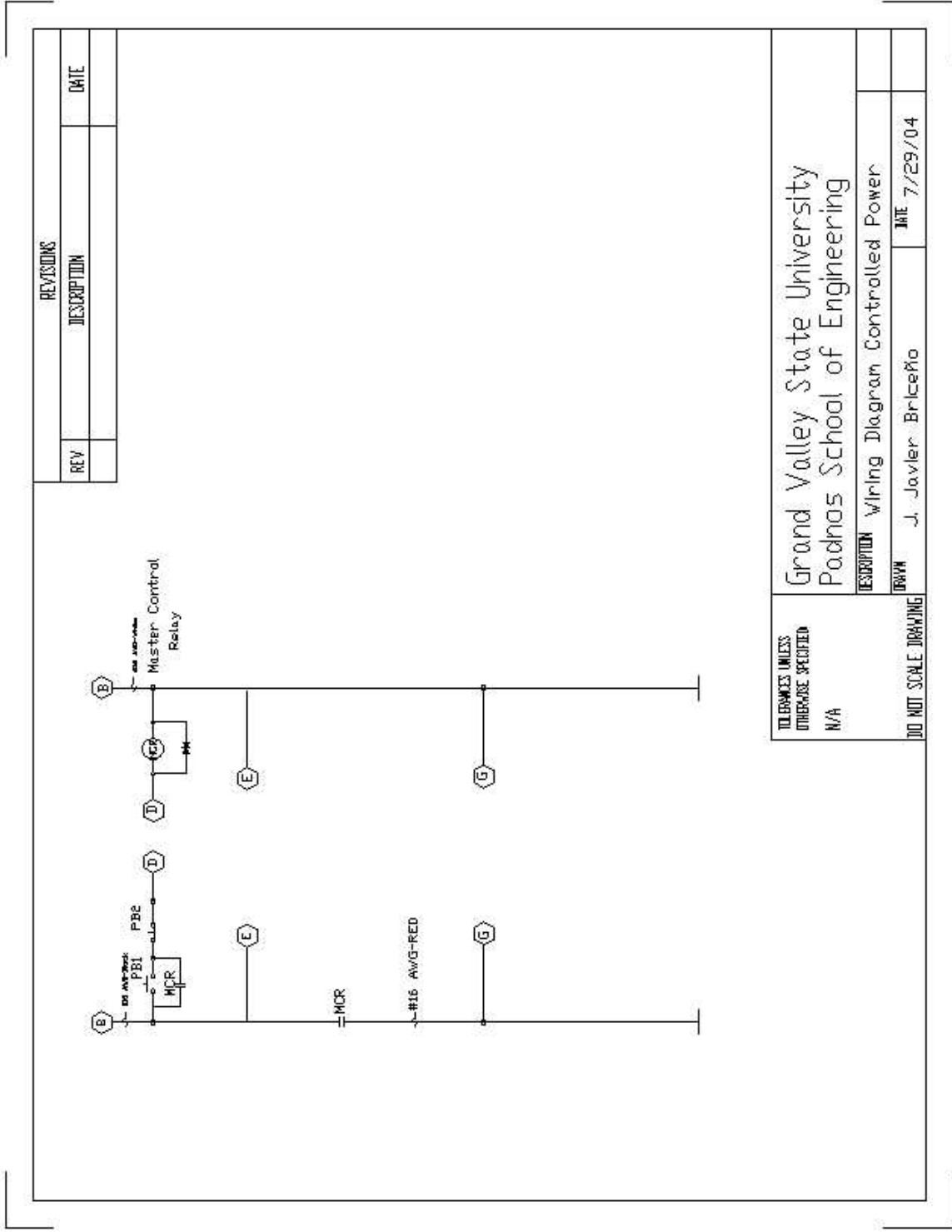
REVISIONS		
REV	DESCRIPTION	DATE

TELETYPE UNLESS OTHERWISE SPECIFIED
N/A

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Grand Valley State University
Padnos School of Engineering

DESCRIPTION: Wiring Diagram - Quickpanel Touch Screen
DRAWN: J. Javier Briceño
DATE: 7/29/04



REVISIONS		
REV	DESCRIPTION	DATE

TELETYPE UNLESS OTHERWISE SPECIFIED N/A	Grand Valley State University Padnos School of Engineering
DESCRIPTION	Wiring Diagram Controlled Power
DRAWN	J. Javier Briceño
DATE	7/29/04

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